SSC8LA20GT4

N-Channel Enhancement Mode MOSFET

➢ Features

V_{DS}	V_{GS}	$R_{DS(ON)}$ Typ.	I_D
100V	±20V	4.2mΩ@10V	88A

➢ Description

This device is N-Channel enhancement MOSFET. Uses SGT technology and design to provide excellent RDSON with low gate charge. This device is suitable for use in DC-DC conversion, power switch and charging circuit.

100% UIS + ΔVDS + Rg Tested!

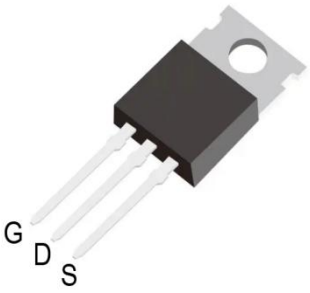
➢ Applications

- Load Switch
- PWM Application
- Power Management
- DC-DC Conversion

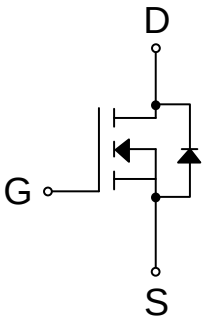
➢ Ordering Information

Device	Package	Shipping
SSC8LA20GT4	TO-220-3L	50/Tube

➢ Pin configuration



TO-220-3L (Top View)



Pin Configuration

Marking

(XXYY: Internal Traceability Code)

➢ Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Symbol	Parameter		Ratings	Unit
V_{DSS}	Drain-to-Source Voltage		100	V
V_{GSS}	Gate-to-Source Voltage		±20	V
I_D	Continuous Drain Current [d]	T_C=25℃	88	A
		T_C=100℃	56	
I_{DSM}	Continuous Drain Current [a]	T_A=25℃	10	A
		T_A=70℃	6.5	
I_{DM}	Pulsed Drain Current [b]		352	A
P_D	Power Dissipation [c]	T_C=25℃	167	W
		T_C=100℃	67	
P_{DSM}	Power Dissipation [a]	T_A=25℃	2.3	W
		T_A=70℃	0.9	
I_{AS}	Avalanche Current [b] L=0.5mH Single Pulse		56	A
E_{AS}	Avalanche Energy [b] L=0.5mH Single Pulse		784	mJ
T_J	Operation junction temperature		-55~150	℃
T_{STG}	Storage temperature range		-55~150	

➢ Thermal Resistance Ratings (T_A=25℃ unless otherwise noted)

Symbol	Parameter	Ratings	Max.	Unit
$R_{\theta JA}$	Junction-to-Ambient Thermal Resistance [a]	55	65	℃/W
$R_{\theta JC}$	Junction-to-Case Thermal Resistance	0.75	1.0	

Note:

a. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in^2 FR-4 board with 2oz.copper, in a still air environment with T_A=25℃.The value in any given application depends on the user is specific board design. The power dissipation is based on the t≤10s thermal resistance rating.

b. Repetitive rating, pulse width limited by junction temperature.

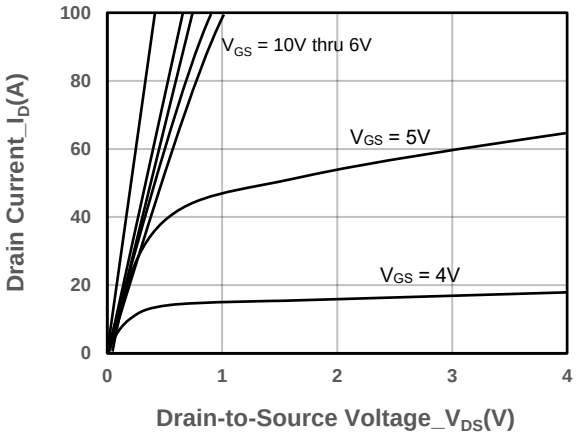
c. The power dissipation P_D is based on $T_{J(MAX)}$=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heat sinking is used.

d. The maximum current rating is package limited.

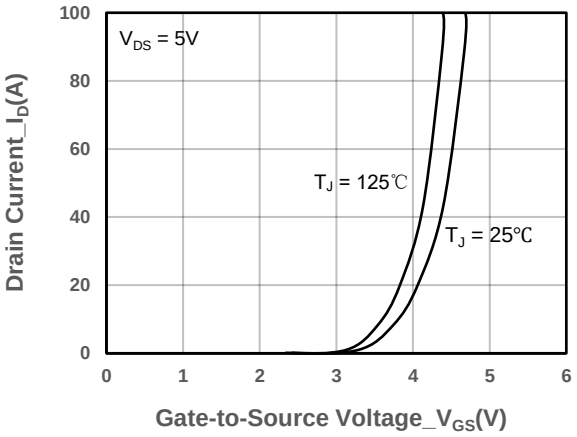
➢ Electrical Characteristics (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D = 250μA	100			V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250uA	2	2.9	4	V
Drain-Source On-Resistance	$R_{DS(on)}$	V_{GS} = 10V, I_D = 20A		4.2	5.5	mΩ
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 100V, V_{GS} = 0V			1	μA
Gate-Source Leak Current	I_{GSS}	V_{GS} = ±20V, V_{DS} = 0V			±100	nA
Forward Voltage	V_{SD}	V_{GS} = 0V, I_S = 20A		0.9	1.4	V
Gate Resistance	R_G	V_{DS} = 0V, f = 1MHz		1.9		Ω
Input Capacitance	C_{ISS}	V_{DS} = 50V, V_{GS} = 0V, f = 1MHz		5200		pF
Output Capacitance	C_{OSS}			1400		
Reverse Transfer Capacitance	C_{RSS}			40		
Total Gate Charge	Q_G	V_{GS} = 10V, V_{DS} = 50V, I_D = 20A		84		nC
Gate to Source Charge	Q_{GS}			22		
Gate to Drain Charge	Q_{GD}			21		
Turn-on Delay Time	$T_{D(ON)}$	V_{GS} = 10V, V_{DS} = 50V, I_D = 20A, R_G = 3Ω,		20		ns
Rise Time	T_r			34		
Turn-off Delay Time	$T_{D(OFF)}$			70		
Fall Time	T_f			45		
Diode Recovery Time	T_{rr}	I_F=20A, di/dt=500A/us		80		ns
Diode Recovery Charge	Q_{rr}	I_F=20A, di/dt=500A/us		170		nC

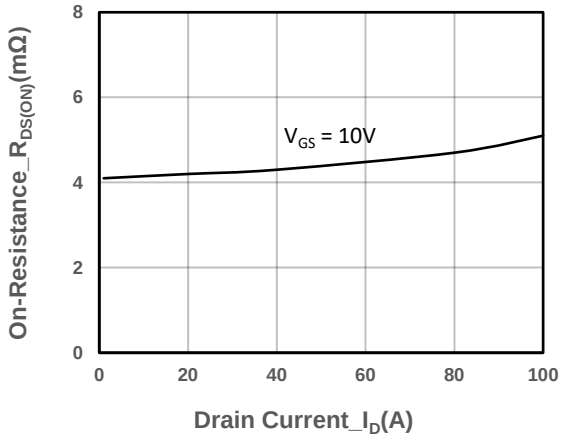
➢ Typical Performance Characteristics (T_A=25℃ unless otherwise noted)



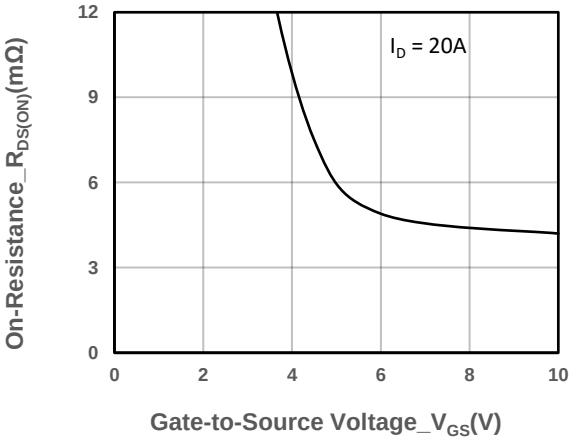
Output Characteristics



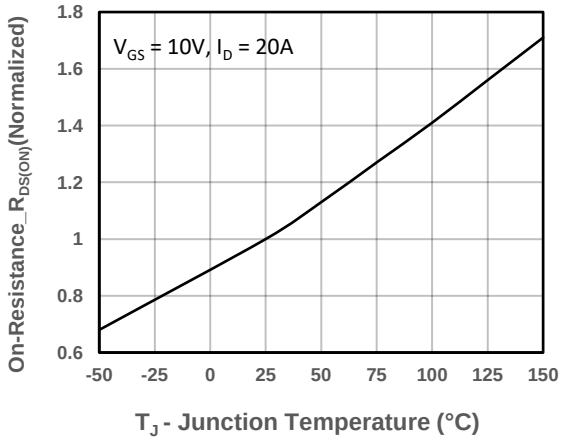
Transfer Characteristics



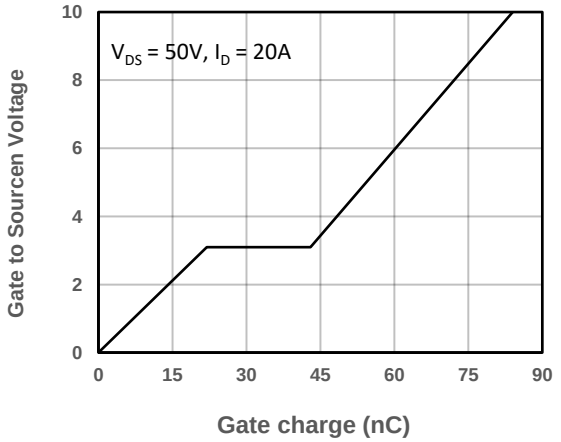
On-Resistance vs. Drain Current and Gate Voltage



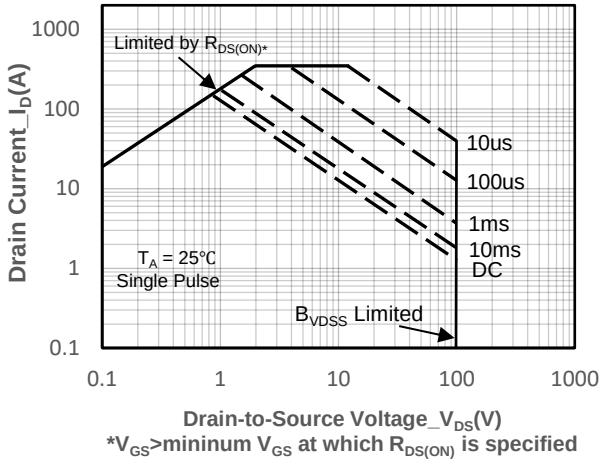
On-Resistance vs. Gate-to-Source Voltage



On-Resistance vs. Junction Temperature

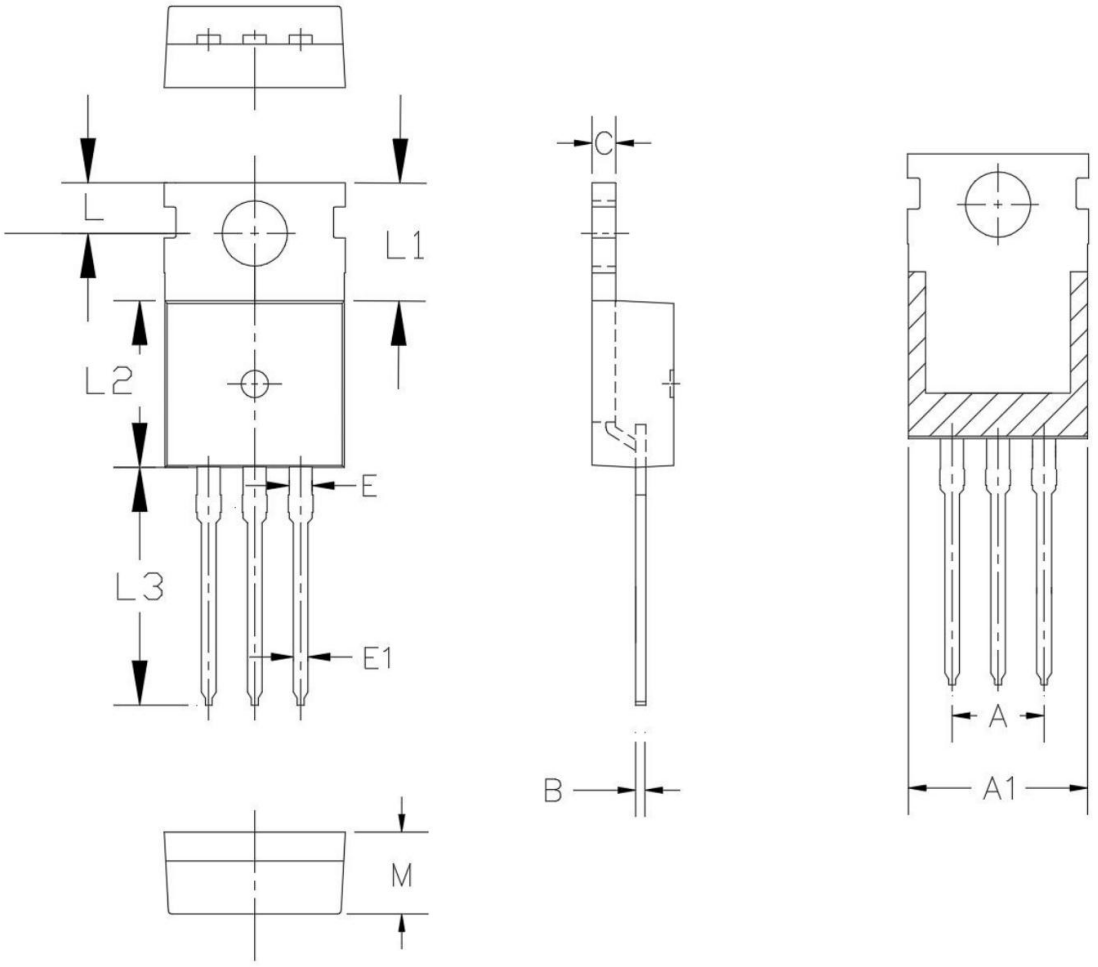


Gate-Source Voltage vs. Gate charge



Safe Operating Area vs. Junction-to-Ambient

➢ Package Information



Symbol	MILL IMETER		
	Min	Nom	Max
A		5.08 BSC	
A1	9.00	10.00	11.00
B	0.33	--	0.65
C	1.20	--	1.40
E	1.17	--	1.37
E1	0.60	--	1.10
L	2.50	--	3.00
L1	6.3	6.5	6.7
L2	8.95	--	9.75
L3	12.88	--	13.40
M	4.30	--	4.70

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